ABSTRACT OF THE DISCLOSURE

Device for processing digital data and, more particularly, for reading out the maximum or minimum value of data belonging to a set of 2^n codes in which a relation of order is established and in which each of said data has a rank R comprised between 0 and 2^n-1. The device includes a conversion circuit for each digital data to be processed, which circuit generates a transform which is a binary number composed of 2^n-1 binary elements T[x] with x = 1 to 2^n-1

$$T[2^n-1]\ T[2^n-2]\ \ldots T[x]\ldots\ T[2]\ T[1]$$

in which T(x) = 0 when x is strictly higher than R and T(x) = 1 when x is lower or equal to R. The result of the conversions is received by circuits that carry out a digital processing thereof.